

#2 Pre-Audit
A 4.50 Cw

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. P043D2C3C)

In the Application of:)
FARMWALD ET AL.) Group
Serial No: Continuation of 09/252,997) Art Unit:
Filed: JANUARY 27, 2000) Before
Title: INTEGRATED CIRCUIT I/O USING A) Examiner:
HIGH PERFORMANCE BUS INTERFACE)

Assistant Commissioner for Patents
Washington, DC 20231

PRELIMINARY AMENDMENT

Dear Sir:

Prior to the examination of the above-referenced application,
kindly amend the application as follows:

IN THE ABSTRACT:

Please delete the Abstract of the Disclosure and substitute
the attached Abstract of the Disclosure.

IN THE TITLE:

A2 Please delete the title and substitute -METHOD OF OPERATING
A MEMORY DEVICE HAVING A VARIABLE DATA INPUT LENGTH -.

IN THE SPECIFICATION:

A3 On page 1, line 8, insert -This application is a continuation
of Application No. 09/252,997 (still pending), which is a
now U.S. Patent No. 6,034,918
Filed 2/19/99

